File: Download.doc

Downloading Data Sheets from the Internet

Many manufacturers have data sheets available for download from the Internet. Most store the data sheets in the common PDF format that can be read using a reader such as the Adobe Acrobat Reader. Texas Instruments has one of the best sites for accessing and downloading data sheets. An example is shown below.

Downloading the Adobe Acrobat Reader

If you do not already have the Adobe Acrobat Reader, you can download it from the following site:

http://www.adobe.com/prodindex/acrobat/main.html

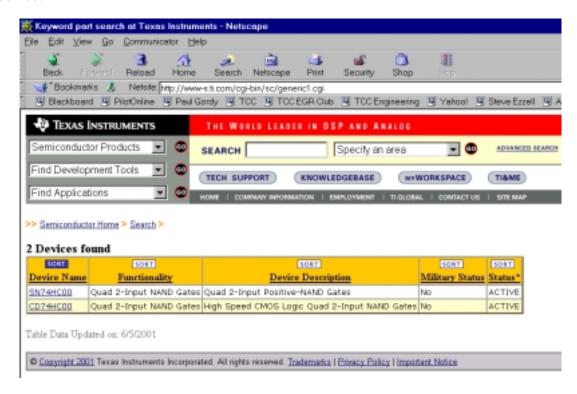
Locating and Downloading Data Sheets from Texas Instruments

 Connect to the following site: http://www-s.ti.com/sc/docs/psheets/pids2.htm

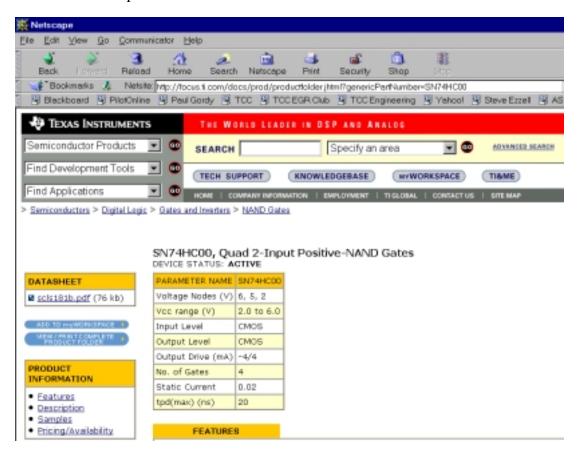
In my example below, note that I entered 74HC00 as a device number and then clicked **on Find Documents**.



2. The next screen shows the results of the search. Click on **SN74HC00** for information on that device.



3. The next screen shows some basic information for the device. For a complete data sheet in Adobe Acrobat format, click on **scls181b.pdf** to download the device. Once it was downloaded you might save it on your computer as 74HC00.PDF (a more logical name). The data sheet can be printed from within the Adobe Acrobat Reader.



4. The next page shows page 1 of the 5 page data sheet.

SN54HC00, SN74HC00 **QUADRUPLE 2-INPUT POSITIVE-NAND GATES**

SCLS181B - DECEMBER 1982 - REVISED MAY 1997

Package Options Include Plastic Small-Outline (D), Thin Shrink Small-Outline (PW), and Ceramic Flat (W) Packages, Ceramic Chip Carriers (FK), and Standard Plastic (N) and Ceramic (J) 300-mil DiPs

description

logic symbol†

These devices contain four independent 2-input NAND gates. They perform the Boolean function $Y = \overline{A \cdot B}$ or $Y = \overline{A} + \overline{B}$ in positive logic.

The SN54HC00 is characterized for operation over the full military temperature range of -55°C to 125°C. The SN74HC00 is characterized for operation from -40°C to 85°C.

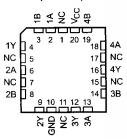
FUNCTION TABLE (each gate)

	INP	UTS	OUTPUT	
Г	Α	В	Y	
Г	Н	н	L	
	L	x	н	
	х	L	н	

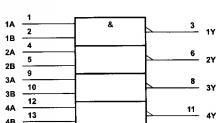
SN54HC00 . . . J OR W PACKAGE SN74HC00 . . . D, N, OR PW PACKAGE (TOP VIEW)

		\Box		L
1A [1	_	14] V _{CC}
1A [1B [2		13] V _{CC}] 4B
1Y [3] 4A
2A [2B [4] 4Y
2B [5		10] 3B
2Y [GND [6		9]] 3A
GND [7		8	3Y

SN54HC00 . . . FK PACKAGE (TOP VIEW)



NC - No internal connection



† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12. Pin numbers shown are for the D, J, N, PW, and W packages.

4B

logic diagram (positive logic)





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